

Table of Contents

Diagrams and Programming Languages for Programmable Controllers	1
<i>Stuart Anderson, Konstantinos Tournas</i>	
Graphical Specification and Reasoning: Case Study Generalised Railroad Crossing	20
<i>Henning Dierks, Cheryl Dietz</i>	
A Graphic Notation for Formal Specifications of Dynamic Systems	40
<i>Gianna Reggio, Mauro Larosa</i>	
A Semantic Integration of Object-Z and CSP for the Specification of Concurrent Systems	62
<i>Graeme Smith</i>	
Class Refinement and Interface Refinement in Object-Oriented Programs .	82
<i>Anna Mikhajlova, Emil Sekerinski</i>	
Formalizing Requirements for Distributed Systems with Trace Diagrams . .	102
<i>Stephan Kleuker</i>	
Consistent Graphical Specification of Distributed Systems	122
<i>Franz Huber, Bernhard Schätz, Gerd Einert</i>	
Design of Reactive Control Systems for Event-Driven Operations	142
<i>K. Lano, A. Sanchez</i>	
An M-Net Semantics for a Real-Time Extension of μ SDL	162
<i>Hans Fleischhack, Josef Tapken</i>	
Reconciling Real-Time with Asynchronous Message Passing	182
<i>M. Broy, R. Grosu, C. Klein</i>	
Specifying the Remote Controlling of Valves in an Explosion Test Environment	201
<i>Martin Schönhoff, Mojgan Kowsari</i>	
PICGAL: Practical Use of Formal Specification to Develop a Complex Critical System	221
<i>Lionel Devauchelle, Peter Gorm Larsen, Henrik Voss</i>	
Mathematical Modeling and Analysis of an External Memory Manager . .	237
<i>William D. Young, William R. Bevier</i>	
Automatic Translation of VDM-SL Specifications into Gofer	258
<i>Paul Mukherjee</i>	

Towards an Integrated CASE and Theorem Proving Tool for VDM-SL	278
<i>Sten Agerholm, Jacob Frost</i>	
Specification of Required Non-determinism	298
<i>K. Lano, J. Bicarregui, J. Fiadeiro, A. Lopes</i>	
A Corrected Failure-Divergence Model for CSP in Isabelle/HOL	318
<i>H. Tej, B. Wolff</i>	
A Proof Obligation Generator for VDM-SL	338
<i>Bernhard K. Aichernig, Peter Gorm Larsen</i>	
Verification of Cryptographic Protocols: An Experiment	358
<i>Marc Mehdi Ayadi, Dominique Bolignano</i>	
TLA + PROMELA: Conjecture, Check, Proof. Engineering New Protocols Using Methods and Formal Notations.	378
<i>J.-Ch. Grégoire</i>	
A TLA Solution to the Specification and Verification of the RLP1 Retransmission Protocol	398
<i>Abdelillah Mokkedem, Michael J. Ferguson, Robert deB. Johnston</i>	
An Efficient Technique for Deadlock Analysis of Large Scale Process Networks	418
<i>J.M.R. Martin, S.A. Jassim</i>	
Implementing a Model Checker for LEGO	442
<i>Shenwei Yu, Zhaohui Luo</i>	
Formal Verification of Transformations for Peephole Optimization	459
<i>A. Dold, F.W. von Henke, H. Pfeifer, H. Rueß</i>	
A Meta-Method for Formal Method Integration	473
<i>Richard F. Paige</i>	
Reuse of Verified Design Templates Through Extended Pattern Matching .	495
<i>David Hemer, Peter A. Lindsay</i>	
A Compositional Proof System for Shared Variable Concurrency	515
<i>F.S. de Boer, U. Hannemann, W.-P. de Roever</i>	
A Framework for Modular Formal Specification and Verification	533
<i>Pierre Michel, Virginie Wiels</i>	
A Timed Semantics for the STATEMATE Implementation of Statecharts .	553
<i>Carsta Petersohn, Luis Urbina</i>	
Using PVS to Prove a Z Refinement: A Case Study	573
<i>David W.J. Stringer-Calvert, Susan Stepney, Ian Wand</i>	

Verification of Reactive Systems Using DisCo and PVS	589
<i>Pertti Kellomäki</i>	
Term Rewrite Systems to Derive Set Boolean Operations on 2D Objects ..	605
<i>David Cazier, Jean-François Dufourd</i>	
A Normal Form Reduction Strategy for Hardware/Software Partitioning ..	624
<i>Leila Silva, Augusto Sampaio, Edna Barros</i>	
Viewpoint Consistency in Z and LOTOS: A Case Study	644
<i>Eerke Boiten, Howard Bowman, John Derrick, Maarten Steen</i>	
A UNITY Mapping Operator for Distributed Programs	665
<i>Michel Charpentier</i>	
Author Index	685