

Contents

1	Introduction	1
2	Preliminaries	3
2.1	Boolean Functions	3
2.1.1	Symmetric Functions	4
2.1.2	Adder Function	4
2.2	Adders	4
2.2.1	Ripple Carry Adder	5
2.2.2	Conditional Sum Adder	6
2.2.3	Carry Look Ahead Adder	7
2.3	Approximate Adders	8
2.4	Error Metrics	9
2.4.1	Bit Threshold	9
2.4.2	Error Rate	9
2.4.3	Average-Case Error	10
2.4.4	Mean-squared Error	10
2.5	Binary Decision Diagrams	11
2.5.1	ITE-Operator	13
2.6	Formal Verification	14
3	Related Work	15
4	Polynomial Verification	17
4.1	General Approximate Functions	17
4.1.1	Polynomially Verifiable Multiplexer Circuit	21
4.1.2	Polynomially Verifiable XOR Circuit	24
4.1.3	Error Metrics	26

4.2	Approximate Adders	29
4.2.1	ETAII	29
4.2.2	GDA	31
4.2.3	ACAI	33
4.2.4	ACAII	34
4.2.5	GeAr	35
4.2.6	Altered Ripple Carry Adder	36
4.2.7	Altered Conditional Sum Adder	41
4.2.8	Altered Carry Look Ahead Adder	46
5	Experiments	51
5.1	General Approximate Functions	51
5.1.1	Experimental Setup	51
5.1.2	Results	53
5.2	Approximate Adders	57
5.2.1	Experimental Setup	57
5.2.2	Results	58
6	Conclusion	75
	Bibliography	77