Table of Contents

Understanding Transactional Memory (Extended Abstract) (Invited Talk)	1
João Lourenço	
The SMT-LIB Initiative and the Rise of SMT (HVC 2010 Award Talk) (Invited Talk)	3
Testing Concurrent Software (Invited Talk)	4
Reflections on Spartan Programming and the No-Debugger Principle (Invited Talk)	5
An Introduction to Test Specification in FQL (Invited Talk) Andreas Holzer, Michael Tautschnig, Christian Schallhart, and Helmut Veith	9
Verification Failures: What to Do When Things Go Wrong (Invited Talk)	23
Special Session on Debugging (Invited Talk)	24
Debugging Unrealizable Specifications with Model-Based Diagnosis Robert Könighofer, Georg Hofferek, and Roderick Bloem	29
Parallelizing a Symbolic Compositional Model-Checking Algorithm Ariel Cohen, Kedar S. Namjoshi, Yaniv Sa'ar, Lenore D. Zuck, and Katya I. Kisyova	46
Reaching Coverage Closure in Post-silicon Validation	60
Variants of LTL Query Checking	76
SAT-Solving Based on Boundary Point Elimination	93



X Table of Contents

Feedback-Based Coverage Directed Test Generation: An Industrial Evaluation	112
vlogsl: A Strategy Language for Simulation-Based Verification of Hardware	129
Advances in Simultaneous Multithreading Testcase Generation Methods	146
Revisiting Synthesis of GR(1) Specifications	161
An Efficient and Flexible Approach to Resolution Proof Reduction Simone Fulvio Rollini, Roberto Bruttomesso, and Natasha Sharygina	182
Author Index	197