

CONTENTS

FOREWORD	vii
1. LINEAR ELECTRIC CIRCUITS	1
1.1. Definition	1
1.2. Fundamental equations	2
1.2.1. Kirchhoff's current law	2
1.2.2. Kirchhoff's voltage law	3
1.3. Graph of an electric circuit	4
1.4. Calculation of dc circuits	6
1.4.1. Method of Kirchhoff's laws	6
1.4.2. Superposition method	7
1.4.3. The equivalent generator method	9
1.4.4. Node voltage method	13
1.4.5. Mesh currents method	16
1.5. Calculation of dynamic circuits	17
1.5.1. Direct integration method	19
1.5.2. Symbolic method	27
1.5.3. Operational method	29
2. TRANSACTOR CIRCUITS	35
2.1. Transactor, gyrator and nullor	35
2.2. Immitance equations	40
2.2.1. Branch equations	40
2.2.2. Circuit equations	43
2.3. Substitution models and diagrams	49
2.4. Precautions in diakoptic treatment	56
3. ANALYSIS BY NODAL METHODS	59
3.1. Introduction	59
3.2. DC analysis	60
3.3. Analysis of variable behaviour	65
3.3.1. Symbolic method for AC linear circuits	65
3.3.2. Operational method	69
3.3.3. Time domain analysis of nonlinear circuits	70

3.3.3.1. Mathematical model	70
3.3.3.2. Systematic formulation of modified nodal equations	80
3.3.3.3. Treatment of excess inductors	82
3.3.3.4. Solving the modified nodal equations	84
3.3.4. Generalized nodal method	87
3.3.4.1. „Capacitor islands” and their treatment	88
3.3.4.2. Elimination of dynamic excess variables	89
3.3.4.3. Systematically identifying of the capacitor islands	91
4. MUTUAL COUPLING MODELING	99
4.1. Models for harmonic regime	99
4.1.1. „Exclusive coupling” two-port	99
4.1.2. „Series”-type model	101
4.1.3. „Parallel”-type model	104
4.1.4. Model with ideal transformer	106
4.1.5. Modeling of couplings in three-phase circuits	107
4.1.6. Modeling of multiple couplings	110
4.1.7. Algorithms and programs	112
4.1.7.1. Algorithm for simple couplings	113
4.1.7.2. Algorithm for multiple couplings	115
4.2. Operational models compatible with nodal analysis	120
4.2.1. Coupled inductors pairs	121
4.2.2. Inductor with multiple coupling	123
4.2.3. Modified nodal equation	124
4.2.4. Algorithm, program and example	125
5. ESTIMATIONS IN RESISTIVE CIRCUITS	131
5.1. Estimation of one-port resistance	131
5.2. Location of iteration starting point	136
6. ANALYSIS BY DISCRETIZED MODELS	143
6.1. Modeling of linear circuits	143
6.1.1. Euler discretized models	143
6.1.2. Models generated by the trapezoidal rule	155
6.1.3. Models associated to Gear discretization	159
6.1.4. General analysis algorithm based on resistive models	164
6.2. Modeling of nonlinear circuits	165
6.2.1. Definition of dynamic parameters	166
6.2.2. Iterative discretized models	167
6.2.3. Discretized models with gyrators	172
6.2.4. Treatment of high complexity nonlinear circuits	176
6.3. Implementation by modified nodal method	179

7. SIMULATION BY WAVEFORM RELAXATION METHODS 191

7.1. Simulation procedure 191

7.2. Partitioning of analyzed circuit 193

7.3. Windowing and scheduling 195

7.4. Diakoptic approach 196

7.5. Segregation in nonlinear circuits 202

8. DOUBLE TIME SCALE METHOD 211

8.1. Method principle 211

8.2. Partial differential equations discretization 214

8.3. Numerical solution 216

9. ANALYSIS BY SIGNAL FLOW GRAPHS 223

9.1. Two-ports 224

9.2. Equivalent transmittances 227

9.3. Node voltages graph 232

9.4. Mesh-current graph 234

9.5. Matrix flowgraphs 237

9.6. Feedback loop method 240

9.7. Optimal topologic method 244

9.8. Acyclic graph method 247

9.9. DC-DC converter graph 252

APPENDICES

Appendix 1. CONNECTION MATRICES 259

Appendix 2. DISCRETIZATION PROCEDURES 267

BIBLIOGRAPHY 271