

Table of Contents

Towards Co-design of HW/SW/Analog Systems	1
<i>Christoph Grimm, Markus Damm, and Jan Haase</i>	
A Flexible Hierarchical Approach for Controlling the System-Level Design Complexity of Embedded Systems	25
<i>Stephan Klaus</i>	
Side-Channel Analysis – Mathematics Has Met Engineering	43
<i>Werner Schindler</i>	
Survey of Methods to Improve Side-Channel Resistance on Partial Reconfigurable Platforms	63
<i>Marc Stöttinger, Sunil Malipatlolla, and Qizhi Tian</i>	
Multicast Rekeying: Performance Evaluation	85
<i>Abdulhadi Shoufan and Tolga Arul</i>	
Robustness Analysis of Watermark Verification Techniques for FPGA Netlist Cores	105
<i>Daniel Ziener, Moritz Schmid, and Jürgen Teich</i>	
Efficient and Flexible Co-processor for Server-Based Public Key Cryptography Applications	129
<i>Ralf Laue</i>	
Cellular-Array Implementations of Bio-inspired Self-healing Systems: State of the Art and Future Perspectives	151
<i>André Seffrin and Alexander Biedermann</i>	
Combined Man-in-the-Loop and Software-in-the-Loop Simulation: Electronic Stability Program for Trucks on the Daimler Driving Simulator	171
<i>Uwe Baake and Klaus Wüst</i>	
Secure Beamforming for Weather Hazard Warning Application in Car-to-X Communication	187
<i>Hagen Stübing and Attila Jaeger</i>	
Author Index	207