

# Table of Contents

|                                                                                                                                                                                                                                |     |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| A Low Cost CMOS Polarimetric Ophthalmoscope Scheme for Cerebral Malaria Diagnostics .....                                                                                                                                      | 1   |
| <i>Xiaojin Zhao, Amine Bermak, and Farid Boussaid</i>                                                                                                                                                                          |     |
| High Performance SoC Design Using Magnetic Logic and Memory .....                                                                                                                                                              | 10  |
| <i>Weisheng Zhao, Lionel Torres, Luís Vitório Cargnini, Raphael Martins Brum, Yue Zhang, Yoann Guilleminet, Gilles Sassatelli, Yahya Lakys, Jacques-Olivier Klein, Daniel Etiemble, Dafiné Ravelosona, and Claude Chappert</i> |     |
| A Low-Power Ultra-Fast Capacitor-Less LDO with Advanced Dynamic Push-Pull Techniques .....                                                                                                                                     | 34  |
| <i>Xin Ming, Ze-kun Zhou, and Bo Zhang</i>                                                                                                                                                                                     |     |
| Improvements to Satisfiability-Based Boolean Function Bi-Decomposition .....                                                                                                                                                   | 52  |
| <i>Huan Chen and Joao Marques-Silva</i>                                                                                                                                                                                        |     |
| Multiplierless Design of Linear DSP Transforms .....                                                                                                                                                                           | 73  |
| <i>Levent Aksoy, Eduardo da Costa, Paulo Flores, and José Monteiro</i>                                                                                                                                                         |     |
| Self-dependent Equivalent Circuit Modeling of Electrostatic Comb Transducers for Integrated MEMS .....                                                                                                                         | 94  |
| <i>Toshiyuki Tsuchiya, Hiroyuki Tokusaki, Yoshikazu Hirai, Koji Sugano, and Osamu Tabata</i>                                                                                                                                   |     |
| On the Functional Test of Branch Prediction Units Based on the Branch History Table Architecture .....                                                                                                                         | 110 |
| <i>Ernesto Sanchez, Matteo Sonza Reorda, and Alberto Paolo Tonda</i>                                                                                                                                                           |     |
| Efficient Multi-rate Hybrid Continuous-Time/Discrete-Time Cascade 2-2 Sigma-Delta Modulators for Wideband Telecom .....                                                                                                        | 124 |
| <i>J. Gerardo García-Sánchez and José M. de la Rosa</i>                                                                                                                                                                        |     |
| A 100dB SFDR 0.5V pk-pk Band-Pass DAC Implemented on a Low Voltage CMOS Process .....                                                                                                                                          | 144 |
| <i>Brendan Mullane and Vincent O'Brien</i>                                                                                                                                                                                     |     |
| Analysis and Design Strategy of On-Chip Charge Pumps for Micro-power Energy Harvesting Applications .....                                                                                                                      | 158 |
| <i>Wing-Hung Ki, Yan Lu, Feng Su, and Chi-Ying Tsui</i>                                                                                                                                                                        |     |
| Author Index .....                                                                                                                                                                                                             | 187 |