

Part 1. Algorithms and Architectures

Towards a Taxonomy for Display Processors <i>Bengt-Olaf Schneider</i>	3
A Hardware Algorithm for Fast Realistic Image Synthesis <i>A.C. Yilmaz, S. Hagestein, E. Deprettere and P. Dewilde</i> ...	37
The HERO Algorithm for Ray-Tracing Octrees <i>Mark Agate, Richard L. Grimsdale and Paul F. Lister</i>	61
A VLSI Architecture for Anti-Aliasing <i>Claudia Romanova and Ulrich Wagner</i>	75
PS: Polygon Streams - A Distributed Architecture for Incremental Computation Applied to Graphics <i>Rajiv Gupta</i>	91
A Generalised Parallel Architecture for Image Based Algorithms <i>G.J. Vaudin, G.R. Nudd, T. J. Atherton, S.C. Clippingdale, N.D. Francis, R.M. Howarth, D.J. Kerbyson, R.A. Packwood and D. Walton</i>	113
Two-level Pipelining of Systolic Array Graphics Engines <i>J.A.K.S. Jayashinge and O.E. Herrmann</i>	133

Part 2. Systems

A Dedicated Graphics Processor SIGHT-2 <i>Masaharu Yoshida, Tadashi Naruse and Tokiichiro Takahashi</i>	151
Viewing and Rendering Processor for a Volume Visualization System <i>Arie Kaufman, Reuven Bakalash and Daniel Cohen</i>	171
Presentation of the Cubi9000: A Graphics System based on Inmos T800 Transputers <i>France Glémot</i>	179
A Virtual Memory System Organization for Bit-Mapped Graphics Displays <i>Anthony C. Barkans</i>	199
A Real-Time Raster Scan Display for 3-D Graphics <i>D. Jackél, H. Günther, B. Herwig and H. Rüsseler</i>	213
The Graphics Unit of the INTEL I80860 <i>Ulrich Kursawe</i>	229
A Chinese-Character and Graphics Workstation <i>Shi Jiaoying, Huang Jianfeng, Liu Liancai and Hu Jingyi</i> ...	249
A Distributed Frame Buffer within a Window-Oriented High Performance Graphics System <i>Thomas Haaker, Harald Selzer and Hans Joseph</i>	261
List of Participants	275