

Contents

Combining model checking and deduction for I/O-automata.....	1
<i>O. Müller and T. Nipkow</i>	
A constraint oriented proof methodology based on modal transition systems.....	17
<i>K. G. Larsen, B. Steffen and C. Weise</i>	
A user guide to HYTECH.....	41
<i>T. H. Henzinger, P.-H. Ho and H. Wong-Toi</i>	
Modal μ -calculus, model checking and Gauß elimination	72
<i>A. Mader</i>	
MONA: Monadic second-order logic in practice	89
<i>J. G. Henriksen, J. Jensen, M. Jørgensen, N. Klarlund, R. Paige, T. Rauhe and A. Sandholm</i>	
Efficient simplification of bisimulation formulas	111
<i>U. H. Engberg and K. S. Larsen</i>	
Hierarchical compression for model-checking CSP or How to check 10^{20} dining philosophers for deadlock	133
<i>A. W. Roscoe, P. H. B. Gardiner, M. H. Goldsmith, J. R. Hulance, D. M. Jackson and J. B. Scattergood</i>	
A front-end generator for verification tools.....	153
<i>R. Cleaveland, E. Madelaine and S. Sims</i>	
Analytic and locally approximate solutions to properties of probabilistic processes	174
<i>C. Tofts</i>	
Model checking of non-finite state processes by finite approximations	195
<i>N. De Francesco, A. Fantechi, S. Gnesi and P. Inverardi</i>	
On automatic and interactive design of communicating systems	216
<i>J. Bohn and S. Rössig</i>	
Layers as knowledge transitions in the design of distributed systems	238
<i>W. Janssen</i>	
Parallelism for free: bitvector analyses $\implies$ no state explosion!.....	264
<i>J. Knoop, B. Steffen and J. Vollmer</i>	
Author Index	291